

FIG. 1

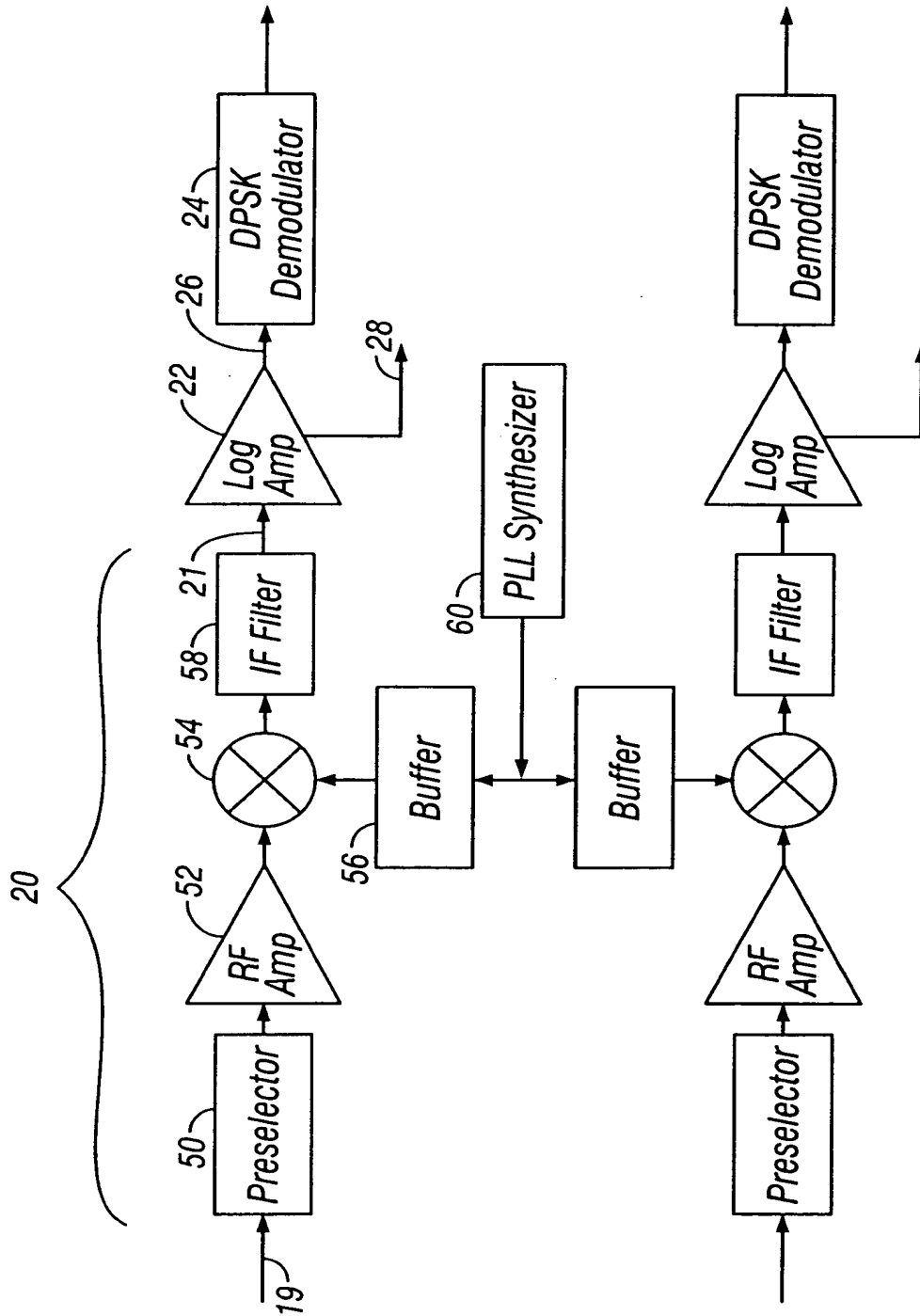


FIG. 2

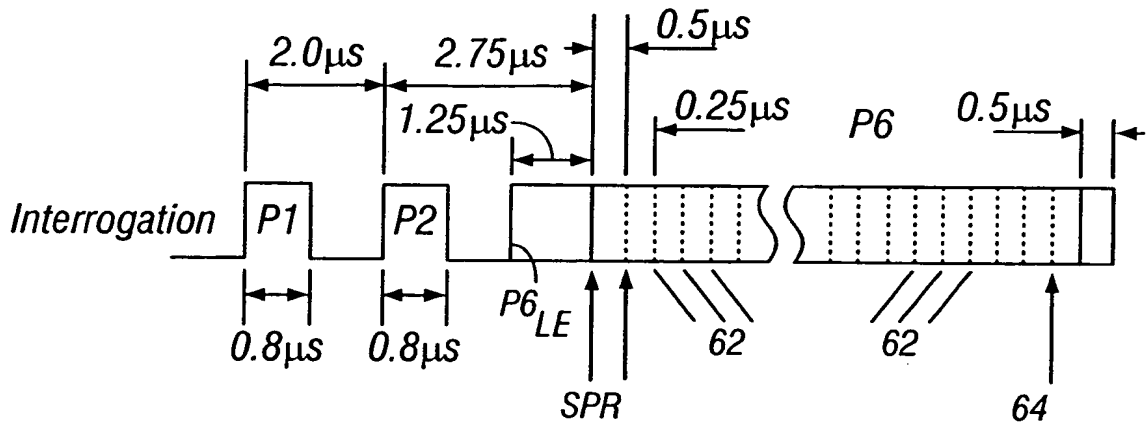


FIG.3

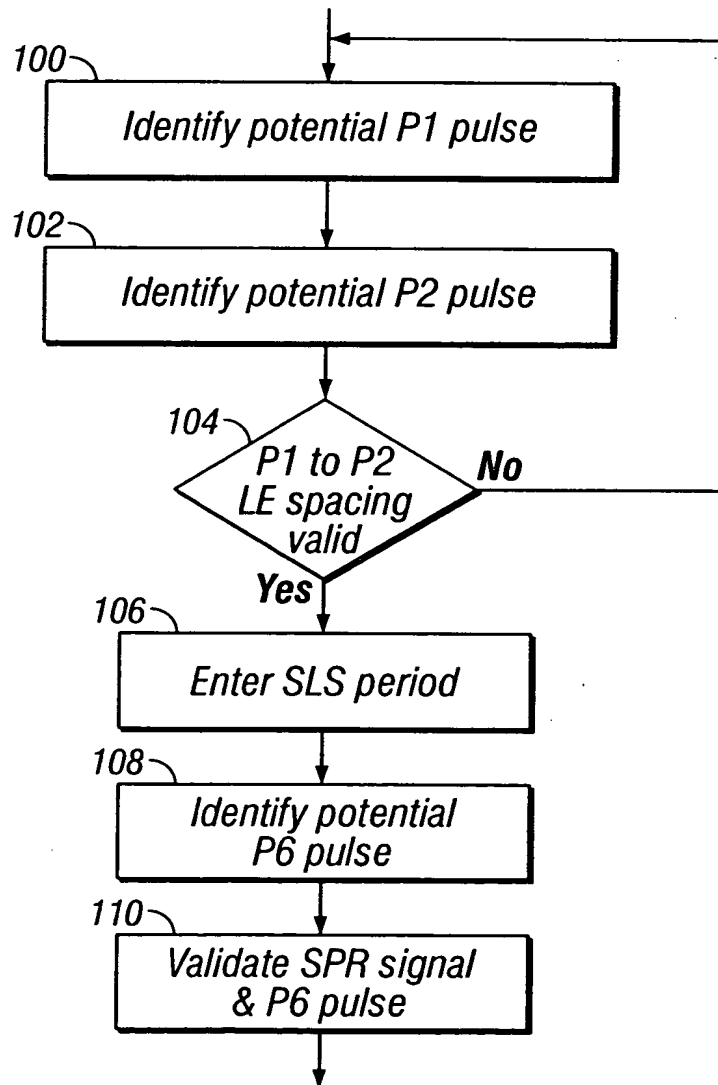


FIG.4

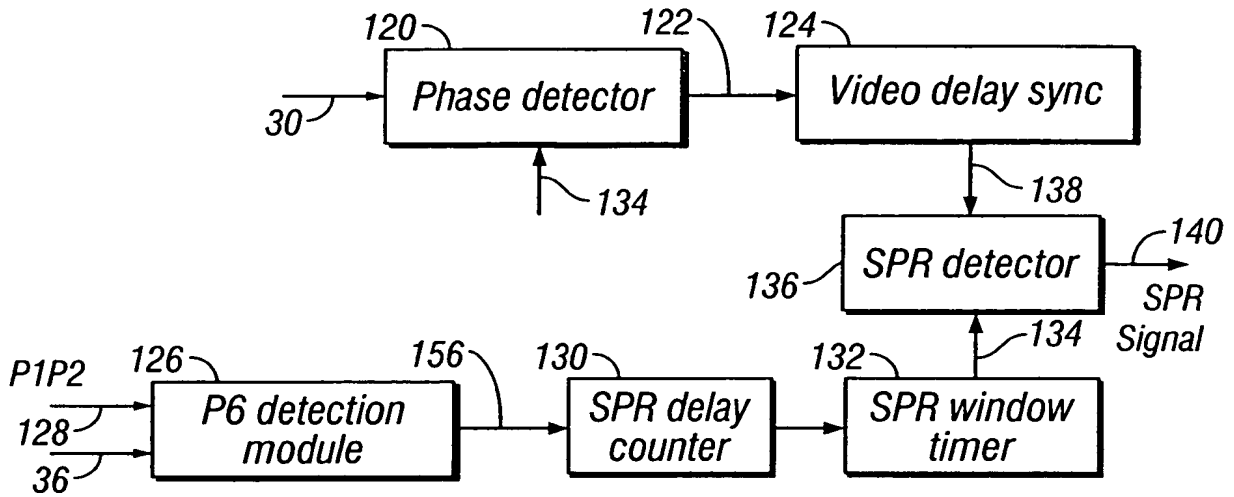


FIG. 5

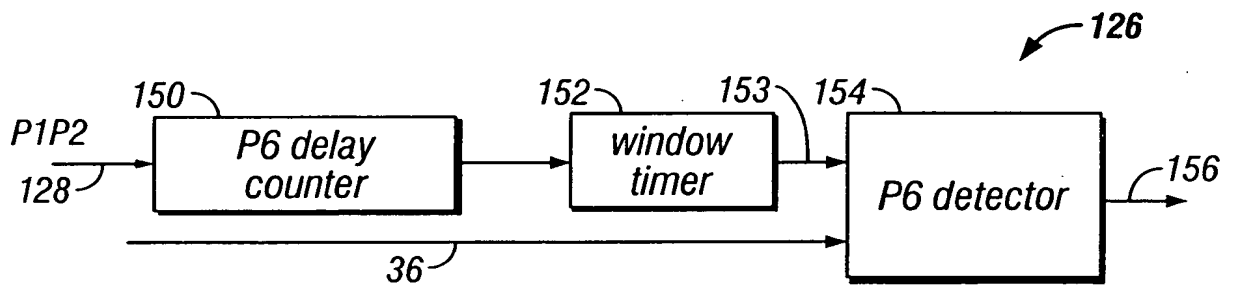


FIG. 6

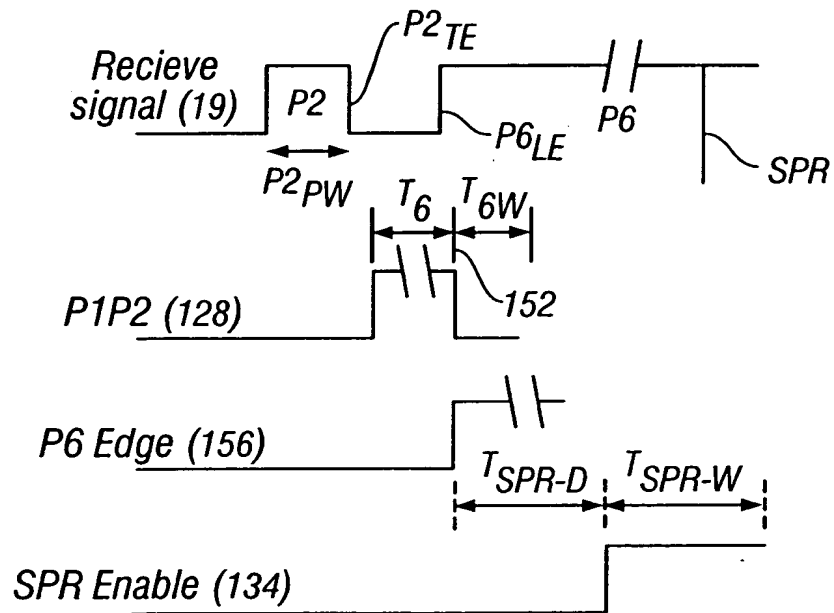


FIG. 7

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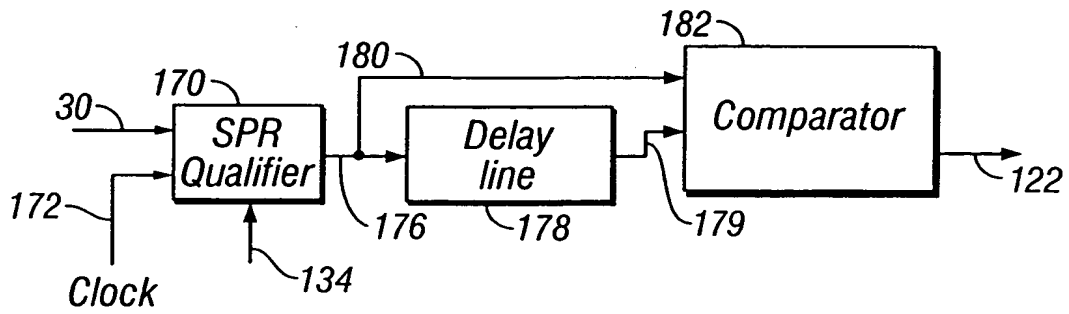


FIG. 8

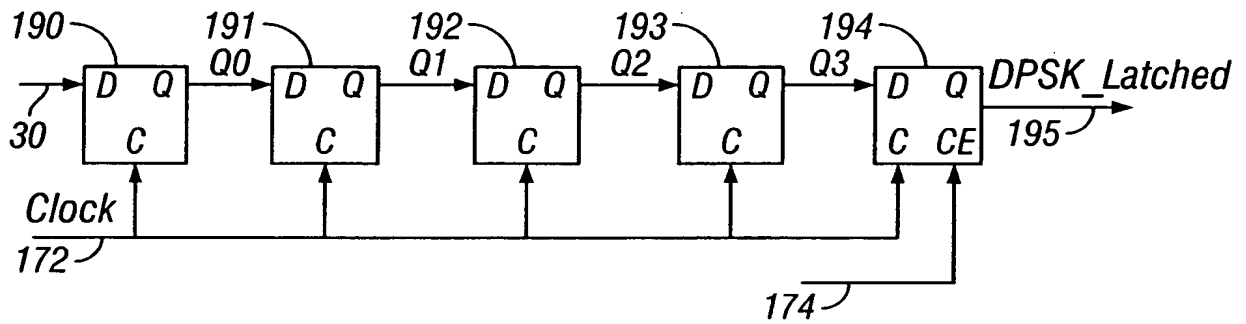


FIG. 9

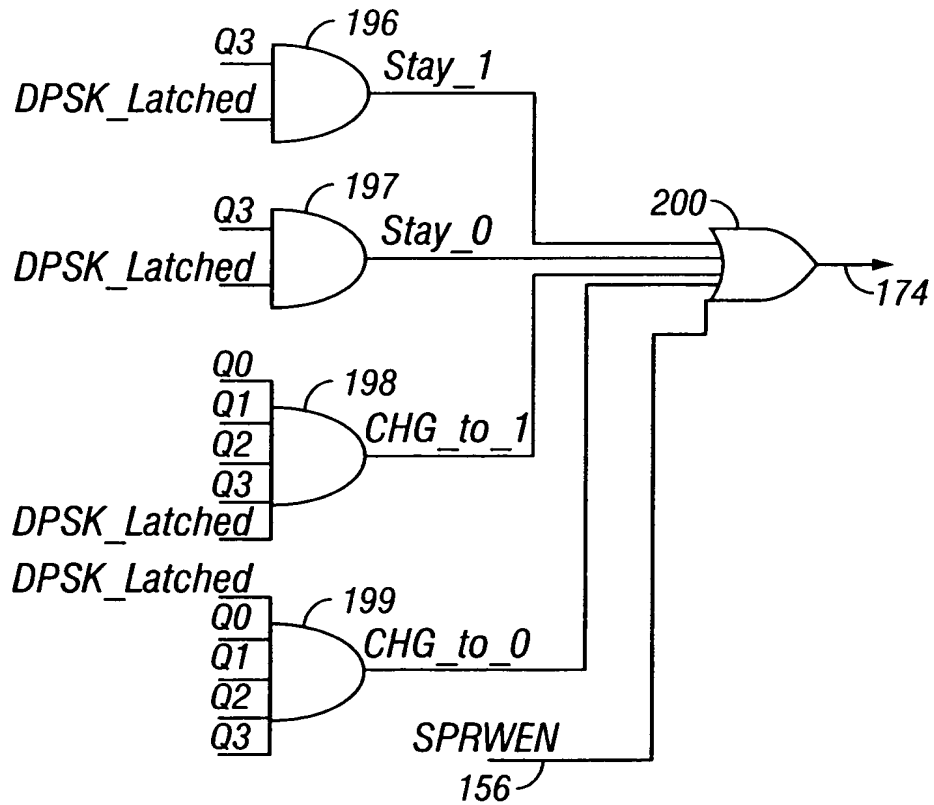
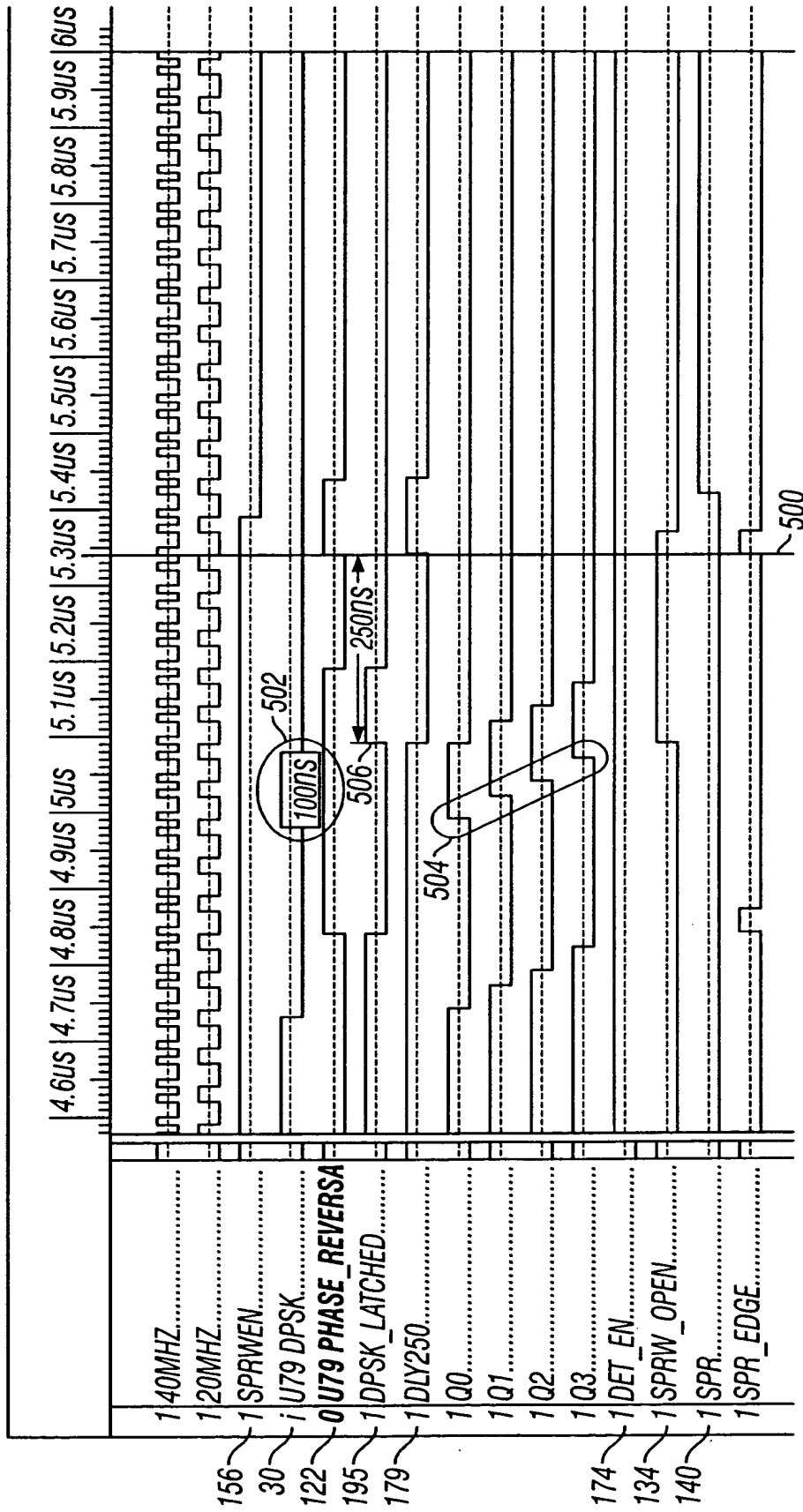
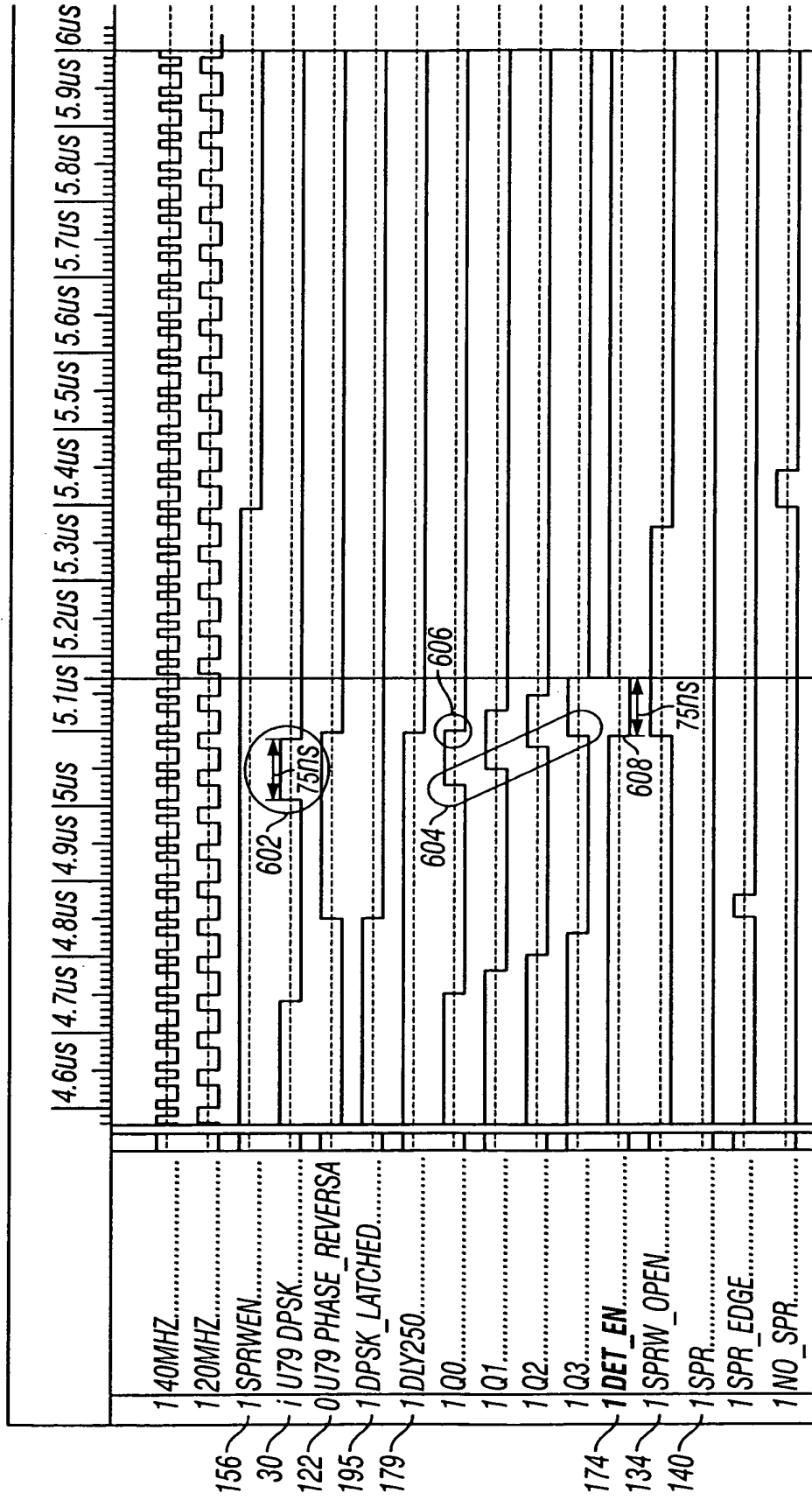


FIG. 10



100nsec pulse is qualified. DET\_EN always high therefore 100nsec pulse passed on to DPSK LATCHED. Then 250nsec later a PHASE REVERSAL and ultimately the SPR\_EDGE is qualified on the SPRW\_OPEN to start SPR. Simulation: SPR100nQ.CMD Bitmap: 100nsqual.bmp

FIG. 11



75nsec DPSK pulse is <100nsec DET\_EN goes low preventing DPSK LATCHED from changing. Therefore no PHASE\_REVERSA detected and ultimately no SPR\_Detection. Preventing noise from causing false SPR detection. Simulation: SPR75nQ.CMD Bitmap: 75nsNOqual.bmp

FIG. 12